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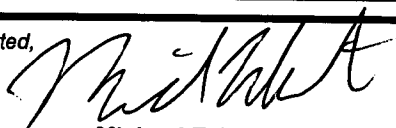
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This is a request for filing a PROVISIONAL APPLICATION FOR PATENT under 37 CFR 1.53(c).22154 U.S. PTO  
60/549935

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TITLE OF THE INVENTION (280 characters max)					
NON-CENTRALIZED CHANNEL STRUCTURES FOR MIDDLEWARE					
Direct all correspondence to: CORRESPONDENCE ADDRESS					
<input checked="" type="checkbox"/> Customer Number		30743		Place Customer Number Bar Code Label here	
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ENCLOSED APPLICATION PARTS (check all that apply)					
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METHOD OF PAYMENT OF FILING FEES FOR THIS PROVISIONAL APPLICATION FOR PATENT (check one)					
<input checked="" type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27.				FILING FEE AMOUNT (\$)	
<input checked="" type="checkbox"/> A check or money order is enclosed to cover the filing fees					
<input checked="" type="checkbox"/> The Director is hereby authorized to charge filing fees or credit any overpayment to Deposit Account Number		50-2041		\$80.00	
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The invention was made by an agency of the United States Government or under a contract with an agency of the United States Government.					
<input checked="" type="checkbox"/> No.					
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Respectfully submitted,

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32,635

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030504

13281 U.S. PTO

## NON-CENTRALIZED CHANNEL STRUCTURES FOR MIDDLEWARE

The invention is a different method to implement object brokering to tie together different software objects (through middleware).

In the implementation of SCA (software communications architecture)-compliant software-defined-radio (SDR), a military standard, data channels are necessarily directed through CORBA, which is a separate process residing in a general-purpose-processor (GPP). A separate channel, under the control of the CORBA ORB or some other middleware, is established between two objects that require a connection. This separate channel can take the form of shared memory between two objects in separate threads within the GPP or some other form of direct channel. By bypassing the ORB( or some other form of middleware) through this channel, several benefits can be reaped:

- highly efficient connection
- easy upgrade path (supported by middleware)
- complements refined software radio design methodology (supports easy partitioning of functionality)
- easier integration of reconfigurable computing platforms
- allows the direct connection of different platforms with little GPP overhead
- isolates reconfigurable computing modules
- eases integration of different reconfigurable computing platforms
- extension of middleware connections outside GPP allows for efficient embodiment of customized connectivity approaches (switching fabrics)
- eases restrictions required to support power management
- eases integration of ASICs cores into system design
- eases development
- Increases scalability of design (by reducing the impact of the GPP bottleneck)

Examples of such functionality can be implemented through strategies such as director memory access or a shared bus.

All current implementations of middleware are designed explicitly to isolated different objects from each other, and, hence use a centralized form of control. By extending the functionality of the middleware into the interface of each of these objects and establishing a

separate (but controlled) data channel, this middleware concept should be far better suited for SDR applications.

This invention significantly reduces the overhead inherent to the implementation of middleware (like an ORB) while at the same time maintaining full compliance with the software designed to use the middleware (like the SCA). It has been shown that an SCA compliant waveform, as much as 76% of the system resources are occupied by the overhead incurred by the middleware. This concept is specifically designed for software-defined-radio implementations, but with the SCA specifically in mind. All new military communications hardware transmitting in channels above 2 GHz are required to use this standard.